



PTO/SB/08A (10/01) (Substitute for form 1449A/PTO)		ATTY. DOCKET NO. 01-CA-341	APPLICATION NO. 10/616,691
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FIRST NAMED INVENTOR: Stefano SAGGINI	
(Use several sheets if necessary) Sheet <u>1</u> of <u>1</u>		FILING DATE July 9, 2003	ART UNIT 2838

### U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document No. No. – Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Doc	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

### FOREIGN PATENT DOCUMENTS

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### OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s) publisher, city and/or country where published
AB		SAGGINI, et al. "A low-complexity high-performance digital control architecture for Voltage Regulator Modules" Proceedings of the 34 <sup>th</sup> IEEE Power of Electronics Specialist Conference, PESC. 2003, Vol. 1, June 15-19, 2003, Mexico, pp. 121-126.

EXAMINER SIGNATURE

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